Amendments to the Specification:

Please replace the paragraph beginning at page 9, line 3, with the following redlined paragraph:

The key memories, cryptographic circuit 9 and data sources and destinations are inter-connected by a network of selectable communication links or data pathways 23. The configuration of the data pathways 23 are determined by a plurality of routing and selection components such as switches 25 and ~~multiplexers~~ multiplexers (MUXs) 27. A switch 25 may be set so that one input pathway is selectively connected to one of a plurality of output pathways according to the setting of the switch 25. A MUX 27 may be set so that one of a plurality of input pathways is selected as an output pathway according to the setting of the MUX 27. Data flows around the system along the pathways 23, and is selectively routed according to the switching states of the routing components connecting the pathways 23. For example, scrambled television signals may be selectively routed from the data-in port 13 to the cryptographic circuit 9, control words may be selectively routed from the control word memory 8 to the cryptographic circuit 9, and the resulting descrambled television data may be selectively routed from the cryptographic circuit 9 to the out port 13.